IN THE SPECIFICATION

Please amend the paragraph starting on page 8, line 8 as follows:

The unique digital signature may be provided through a number of implementations such as (i) a varying frequency signal, (ii) a pulse of different width, (iii) a series of pulses of known width, etc. For example, a Verilog model _(a Hardware Description Language (HDL) model)_ of an analog design (or block) may be used to allow connectivity of the analog pins to be verified in a digital simulator.